## 3 SPECIFICATIONS

The following shows the Q series C24 performance specifications. For general specifications, refer to the QCPU User's Manual (Hardware Design, Maintenance and Inspection).

## 3.1 Performance Specifications

The following shows the Q series C24 performance specifications. For the transmission specifications when communicating via the modem function, see this section and User's Manual (Application).

## (1) Transmission specification

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	ltom	Specifications									
ltem		QJ71C24N QJ71C24			QJ71C24N-R2 QJ71C24-R2		QJ71C24N-R4				
Interface		RS-232-compliance			RS-232-compliance		RS-422/485-compliance				
	CH1	(D-sub 9 pin)			(D-sub 9 pin)		(2-piece plug-in connector socket				
		( 3333 p )			( 3333 )		block)				
	CH2	RS-422/485-compliance			RS-232-compliance		RS-422/485-compliance (2-piece plug-in connector socket				
	OTIZ	(2-piece terminal block)			(D-sub 9 pin)		block)				
Communications system ( * 1)	Line	Full-duplex/half-duplex communications									
	MC protocols	Half-duplex communications									
	communication	naii-dupiex confindrications									
	Non procedure protocol communication	Full-duplex/half-duplex communications									
	Bidirectional protocol	Full-duplex/half-duplex communications									
	communication	i uli-uupiexiiali-uupiex communications									
	Pre-defined protocol	Full-duplex/half-duplex communications									
O	communication ( * 6)										
Synchronization me	etnod	[O 174 C24N/ D	2/D4\1	Sta	rt-stop synchroni	zation method					
		[QJ71C24N(-R 50	300	600	1200	2400	4800	9600	7		
		14400	19200	28800	38400	57600	115200	230400	(bps)		
		-							(ups)		
		Transmission speed 230400 bps is available for only CH1. (Not available for CH2)  Total transmission speed of two interfaces is available up to 230400 bps.									
Transmission speed	d	Total transmission speed of two interfaces is available up to 115200 bps when the communication data									
·		monitoring function is used.									
		[QJ71C24(-R2)]									
			300	600	1200	2400	4800	9600			
		14400	19200	28800	38400	57600	115200	-	(bps)		
		Total transmission speed of two interfaces is available up to 115200 bps.									
Data format	Start bits	1									
	Data bits	7/8									
	Parity bits	1 (vertical parity) or none									
Access cycle	Stop bits	1/2									
	MC protocol communication	Processes one request during installed programmable controller CPU END processing.  * Number of scans that must be processed/number of link scans depends on the contents of the request. (See Reference Manual.)									
	Non procedure protocol	Sends each time a send request is issued. Can receive at any time.									
	communication										
	Bidirectional protocol communication	Contract Sales a contract of the contract of t									
	Pre-defined protocol										
	communication ( * 6)	Sends or receives data when requested with the dedicated instruction (CPRTCL).									
Error detection	Parity check	Selected for all protocols and when this check is enabled, ODD or EVEN is selected by a parameter.									
		For the MC or bidirectional protocol, selected by a parameter.									
	Sum check code	For the non procedure protocol, selected in the user frame.  For the pre-defined protocol, whether or not a sum check code is needed depends on the selected									
		protocol. (*6)									
		protocol. (**)	(ن								

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ltem			Specifications           QJ71C24N         QJ71C24N-R2					
			QJ/1024N QJ71024	QJ/1C24N-R2 QJ/1C24-R2	QJ71C24N-R4			
			Q37 1C24	QJ/ 1024-R2				
				RS-232	RS-422/485			
Transmission control			DTR/DSR control	Enabled	Disabled			
			RS/CS control		Disabled			
				Enabled				
			CD(DCD) signal control	Enabled	Disabled			
			DC1/DC3 (Xon/Xoff) control	Enabled	Enabled			
			DC2/DC4 control     DTR/DSR signal control and DC control	laor.				
Line configuration RS-232			1:1					
(Connection) ( * 2)			1:1 1:1 1:1		1:1, 1:n, n:1, m:n			
(Connection) ( 2)	RS-422/485 MC protocols		1.1, 1.11, 11.1, 111.11	·	1.1, 1.11, 11.1, 11.11			
ı		communication	1:1	1:1				
Ì		Non procedure			†			
1		protocol	1:1	1:1				
	RS-	communication	1.1	1.1				
	232	Bidirectional			† –			
	202	protocols	1:1	1:1				
Line configuration		communication	1.1	1.1				
		Pre-defined protocol			1			
(Data		communication ( * 6)	1:1	1:1				
communication)	RS- 422/ 485	MC protocols						
(* 2)		communication	1:1, 1:n, m:n		1:1, 1:n, m:n			
		Non procedure						
		protocol	1:1, 1:n, n:1		1:1, 1:n, n:1			
		communication	1.1, 1.11, 11.1		1.1, 1.11, 11.1			
		Bidirectional		_				
		protocols	1:1		1:1			
		communication						
		Pre-defined protocol						
		communication ( * 6)	1:1, n:1		1:1, n:1			
Transmississ	RS-232		Maximum 15 m	Maximum 15 m				
Transmission			(49.2 ft.)	(49.2 ft.)	_			
distance (Overall distance)	DC 44	22/405	Maximum 1200 m (3937 ft.)		Maximum 1200 m (3937 ft.)			
uistance)	RS-422/485		(overall distance)	<u> </u>	(overall distance)			
Flash ROM write co	unt		Maximum 100,000 times to the same	area	·			
Number of occupied I/O points			32 points per slot (I/O assignment: Intelli: 32 points) ( * 3)					
	RS-232		7/0. 127 □P HRV-SV Outside diameter 8.5mm (0.33in.) or more					
Recommended			(Oki Electric Cable Co., Ltd. Applicable number is specified in □.)					
	RS-422/485		SPEV (SB)-MPC-0.2 $^{ imes}$ 3P Outside diameter approx. 6.5mm (0.26 in.) (Mitsubishi Cable Industries,					
cable			LTD.)					
			SPEV(SB)-0.2 $^{ imes}$ 3P Outside diameter approx. 7.5mm (0.3 in.) (Mitsubishi Cable Industria					
Applicable connector for external wiring			D-sub 9 pin (male)	_				
5V DC internal curre	nt con	sumption	0.31A	0.39A				
External dimensions			98 (3.86 in.) (H) $^{\times}$ 27.4 (1.08 in.) (W) $^{\times}$ 90 (3.54 in.) (D)[mm]					
Weight			, , , ,	0.20kg (0.44lb)				
			lata da a contana a follosida de	O.Eong (O.TTID)				

- \*1 Set to transfer data with external devices using a full-duplex communication system when the Q series C24 is started. For switching to a half-duplex communication system, refer to the User's Manual (Application).
- \*2 Indicates possible combinations when connecting the programmable controller CPU and external devices (external device side: programmable controller CPU side). The total number of n and m+n is up to 32 stations.
- \*3 In order to use the Q series C24, it is necessary to set the GX Developer switches. To set the GX Developer switches, refer to Section 4.5.
- \*4 Recommended cables SPEV (SB)-MPC-0.2  $\times$  3P and SPEV (SB)-0.2  $\times$  3P are equivalent in the electrical characteristics, but partially different in the outside diameter, internal wire colors, etc.
- \*5 See Section 3.2.1 (3) for the recommended connector.
- \*6 Pre-defined protocol communication is available for the QJ71C24N, QJ71C24N-R2, and QJ71C24N-R4.

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